

Fig. 1

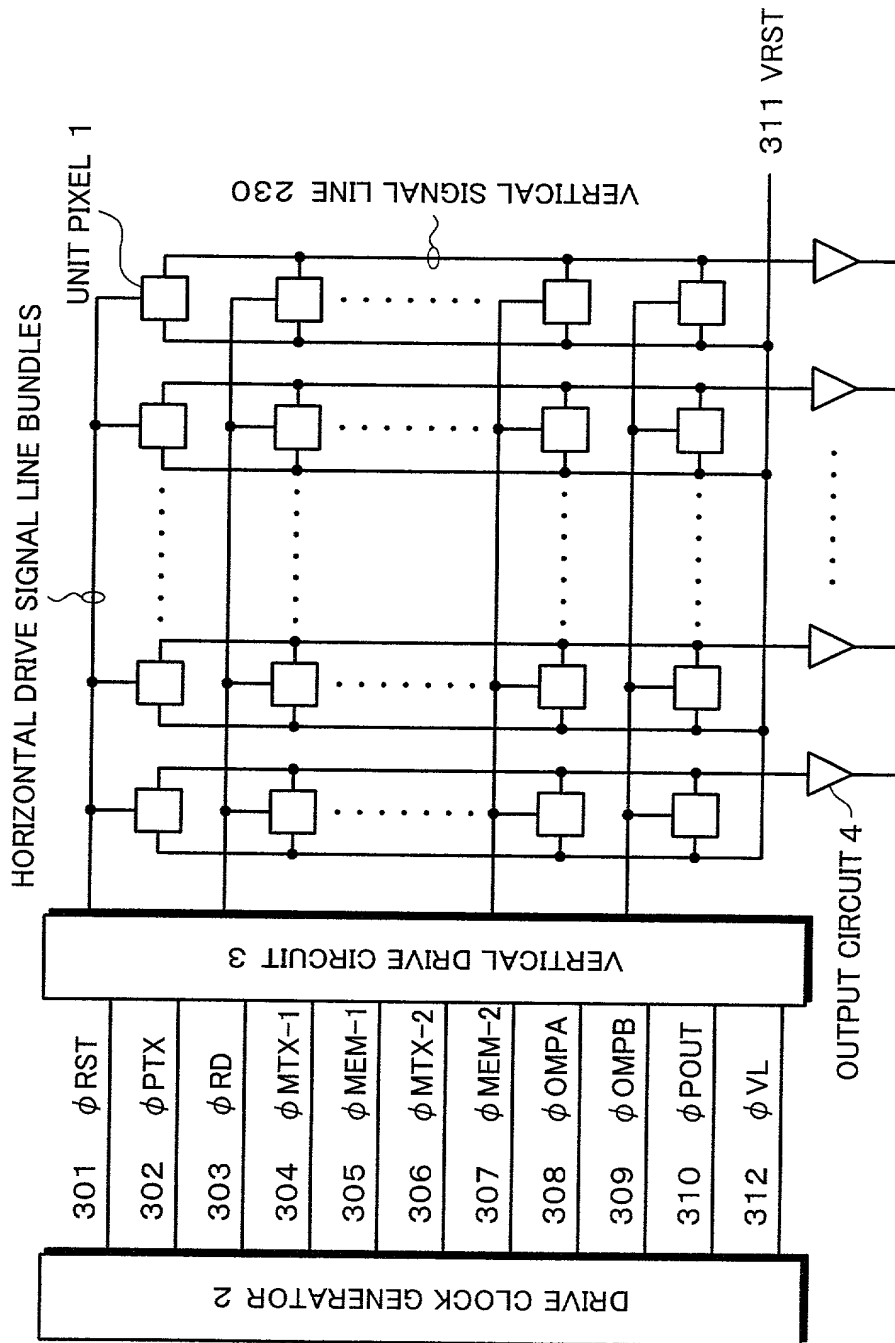
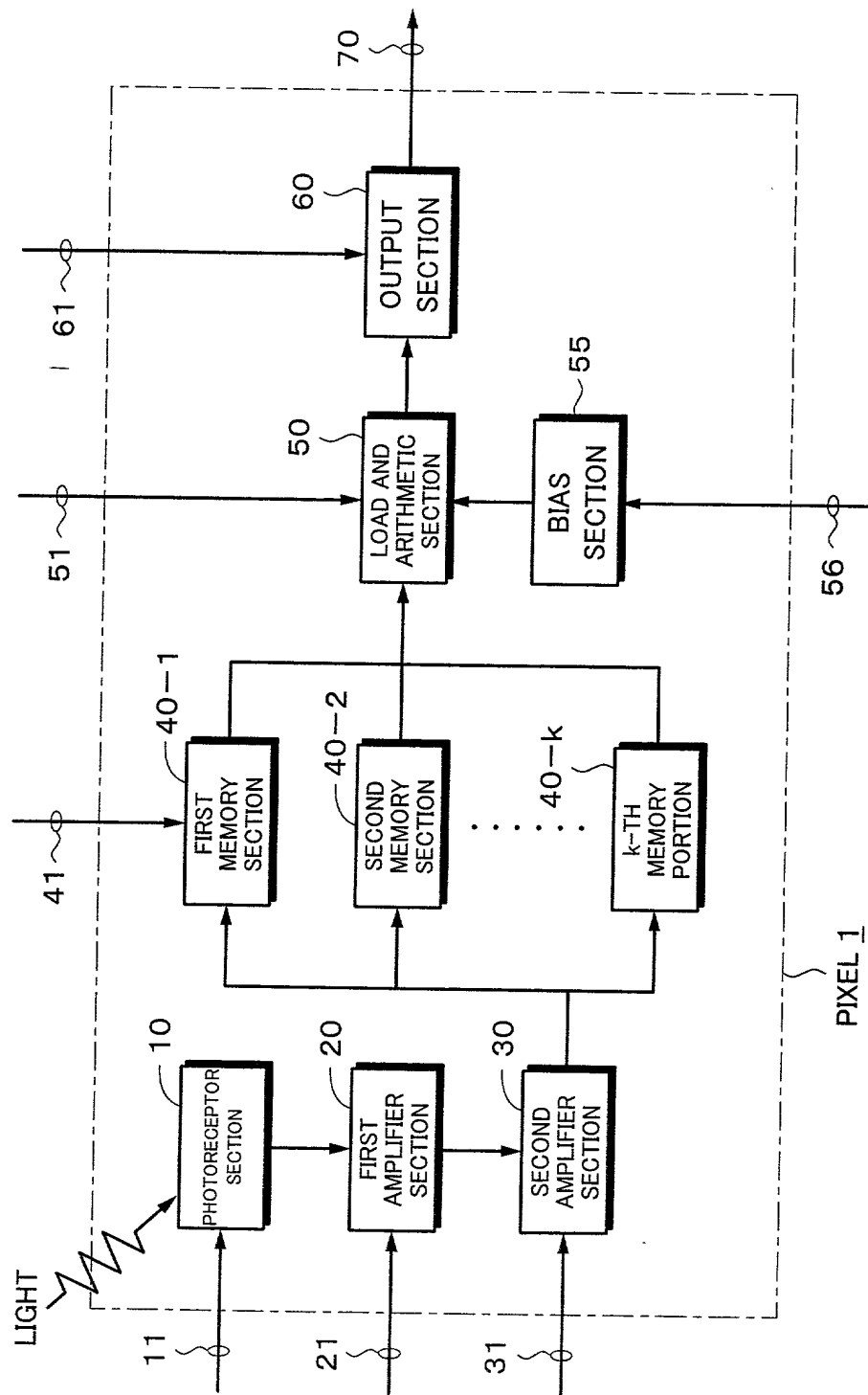


Fig. 2



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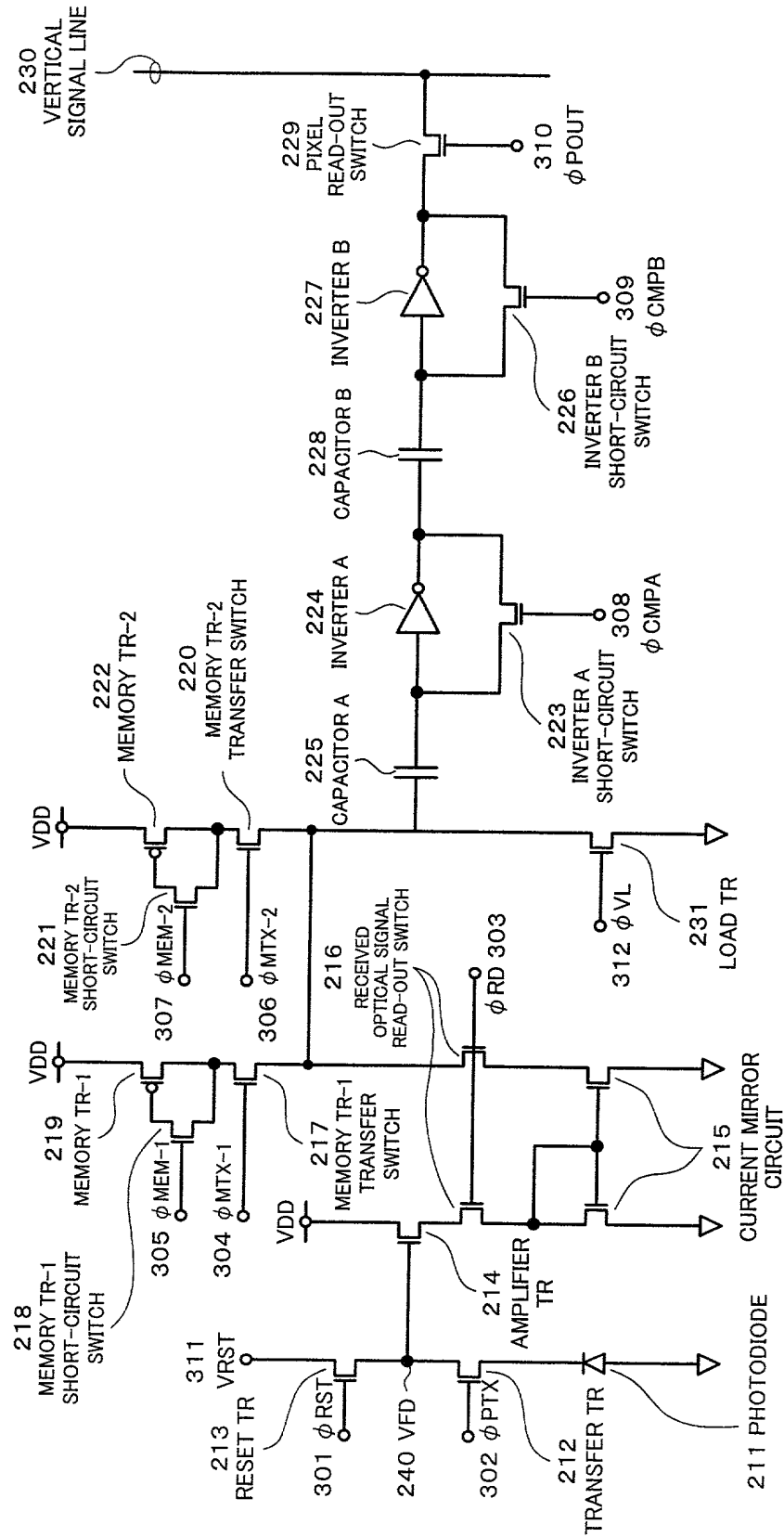


Fig. 4

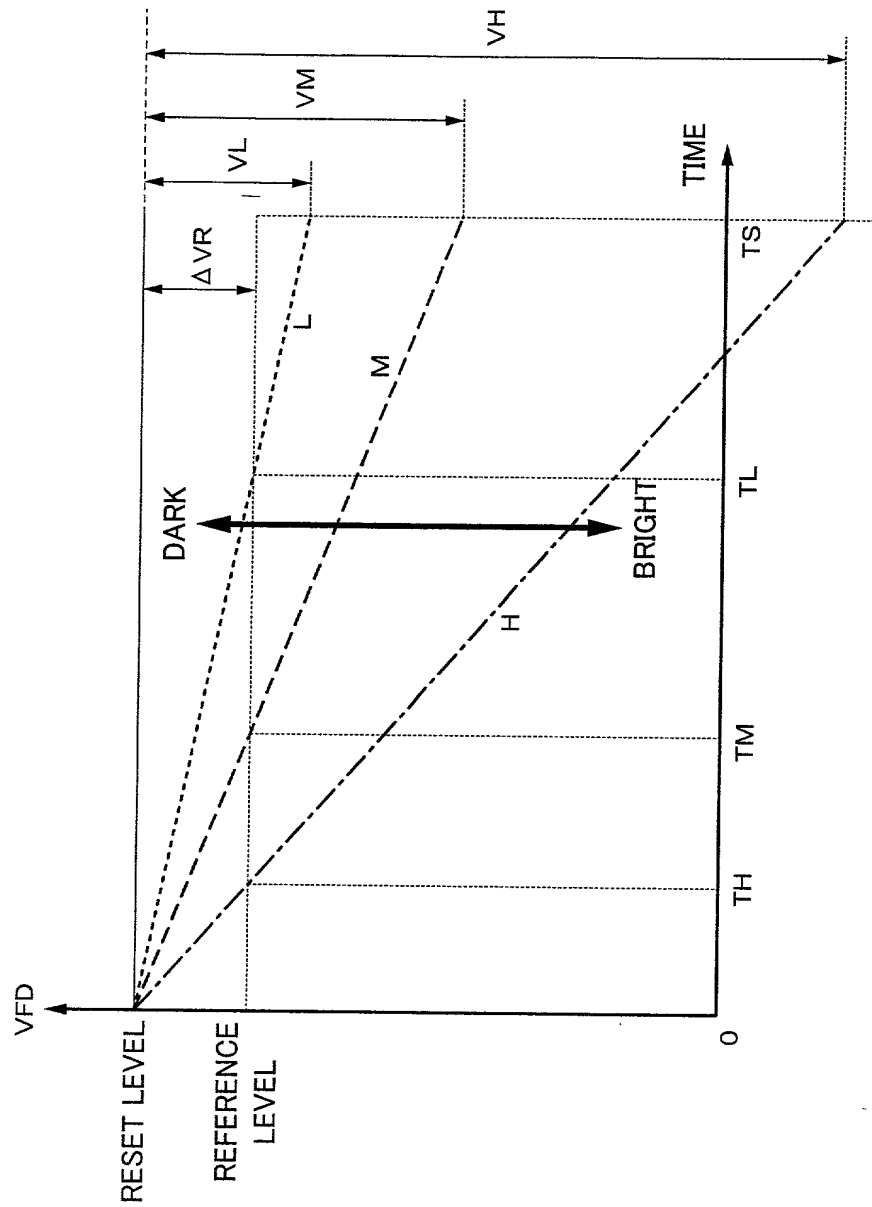


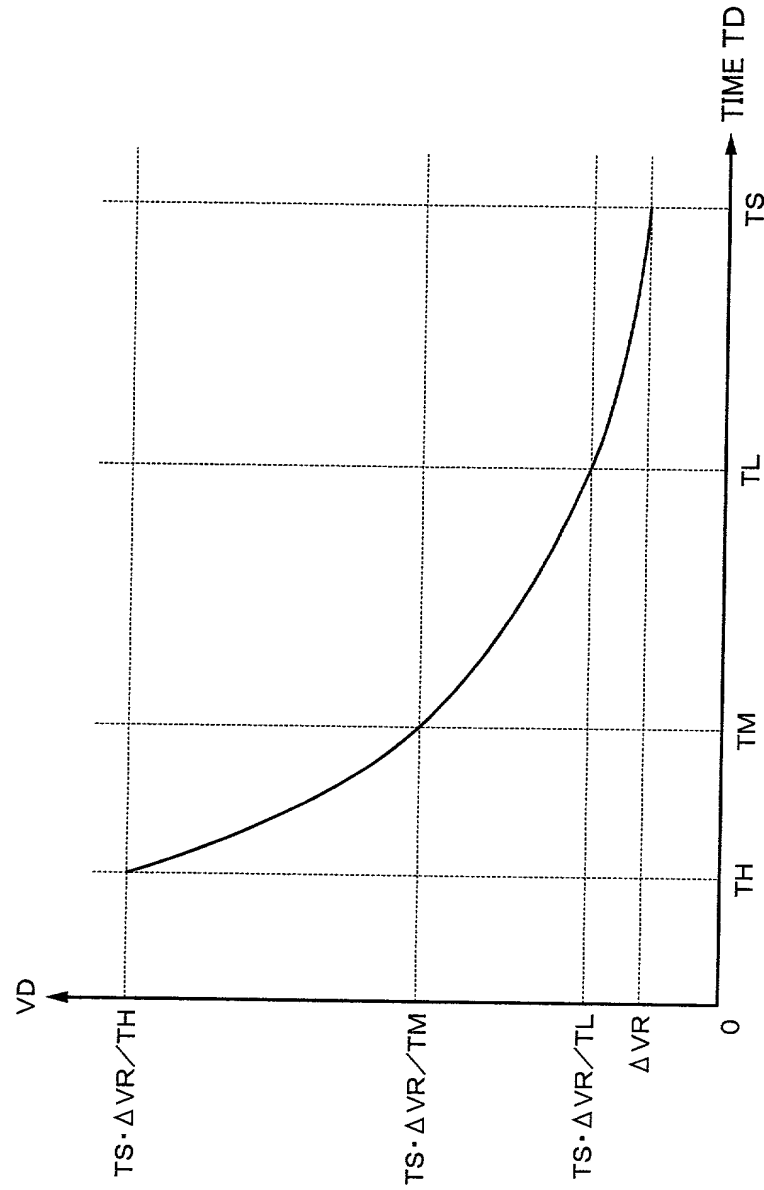
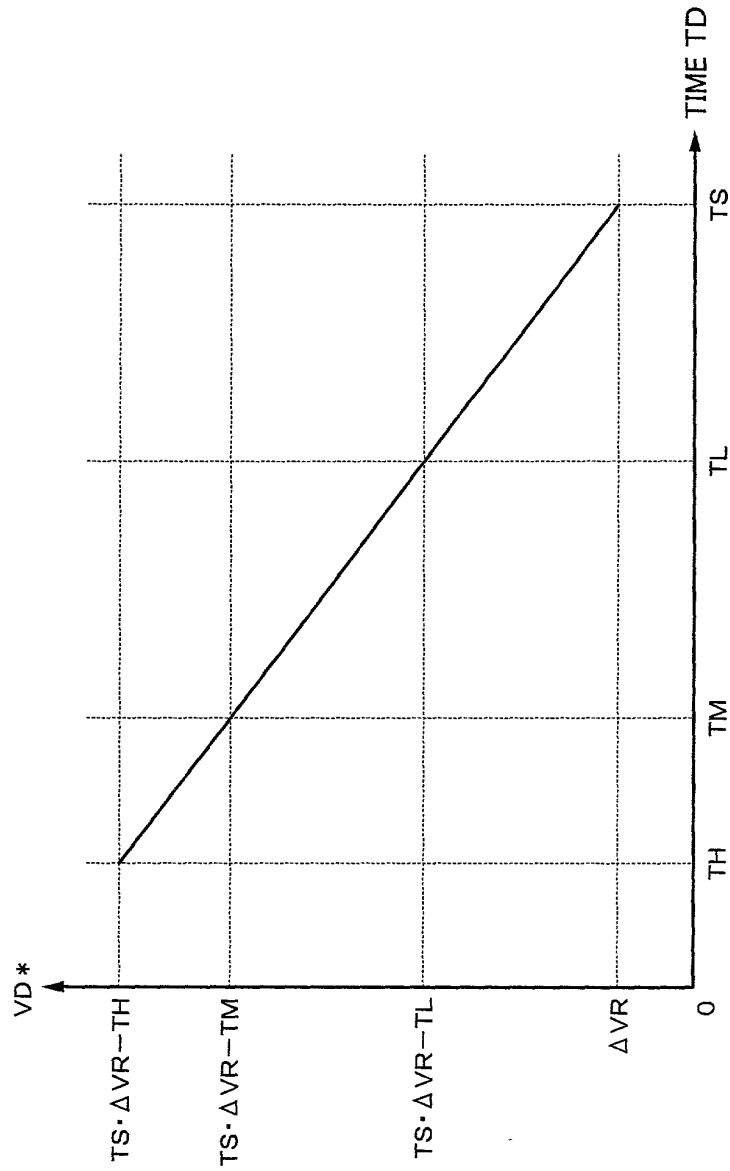
Fig. 5

Fig. 6



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Fig. 7

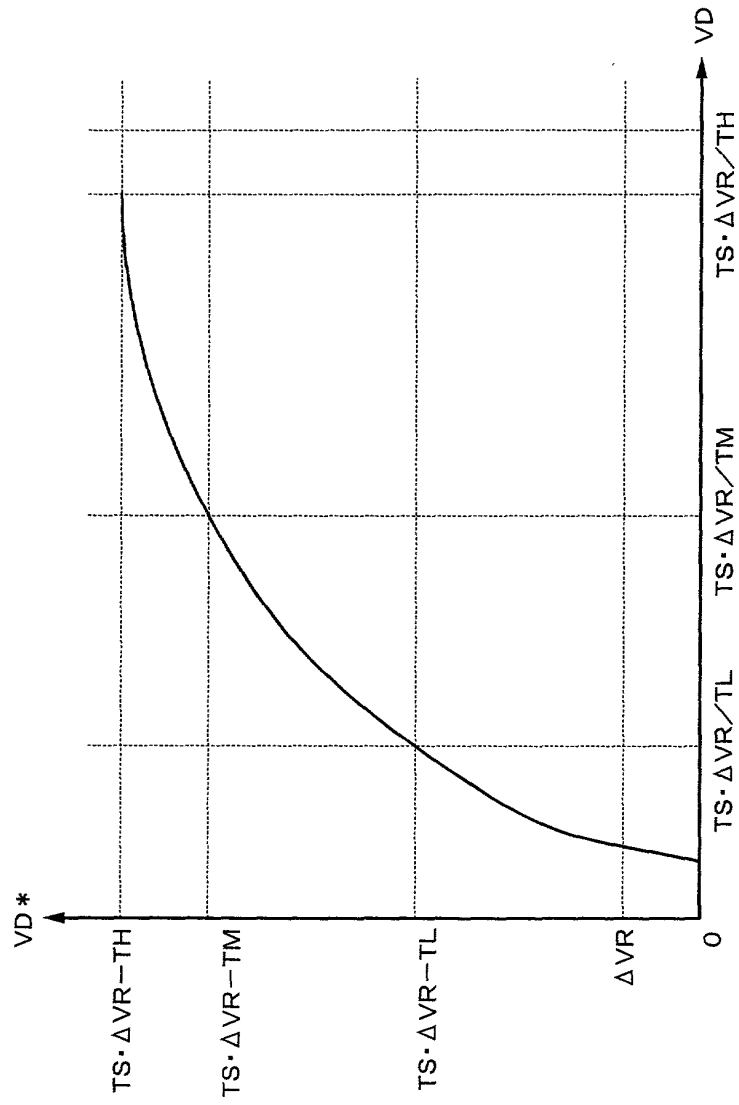


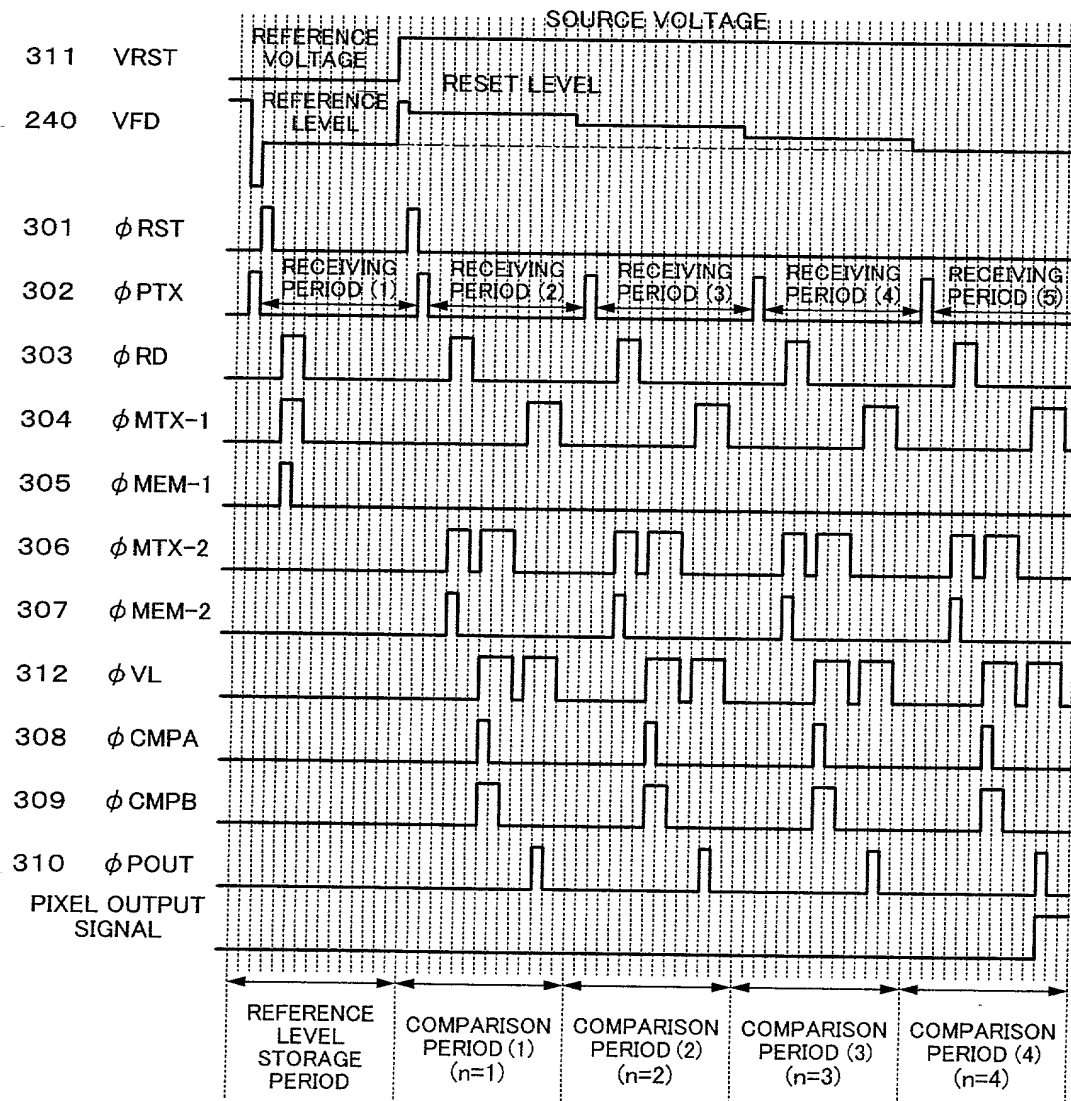
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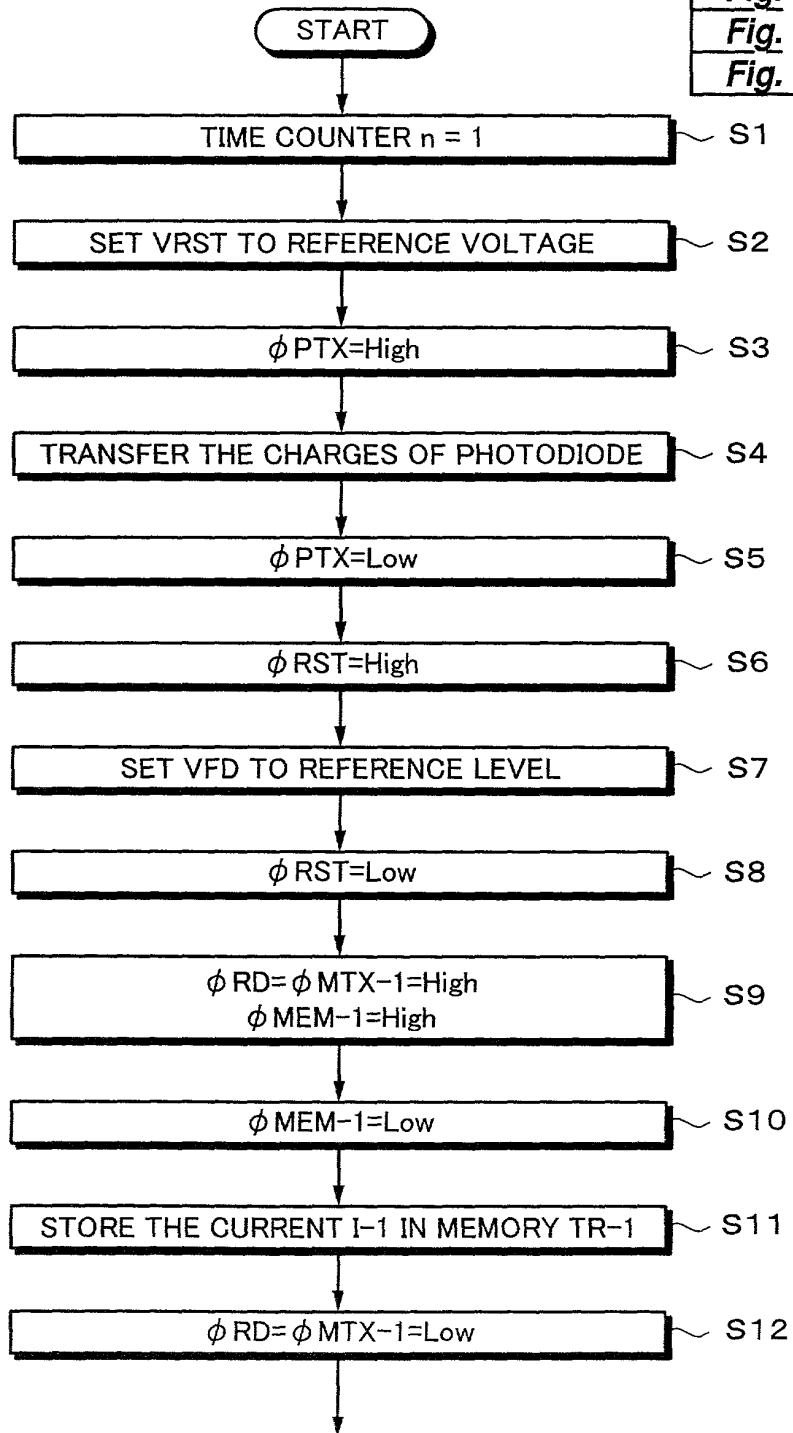
Fig. 9A**Fig. 9****Fig. 9A****Fig. 9B****Fig. 9C**

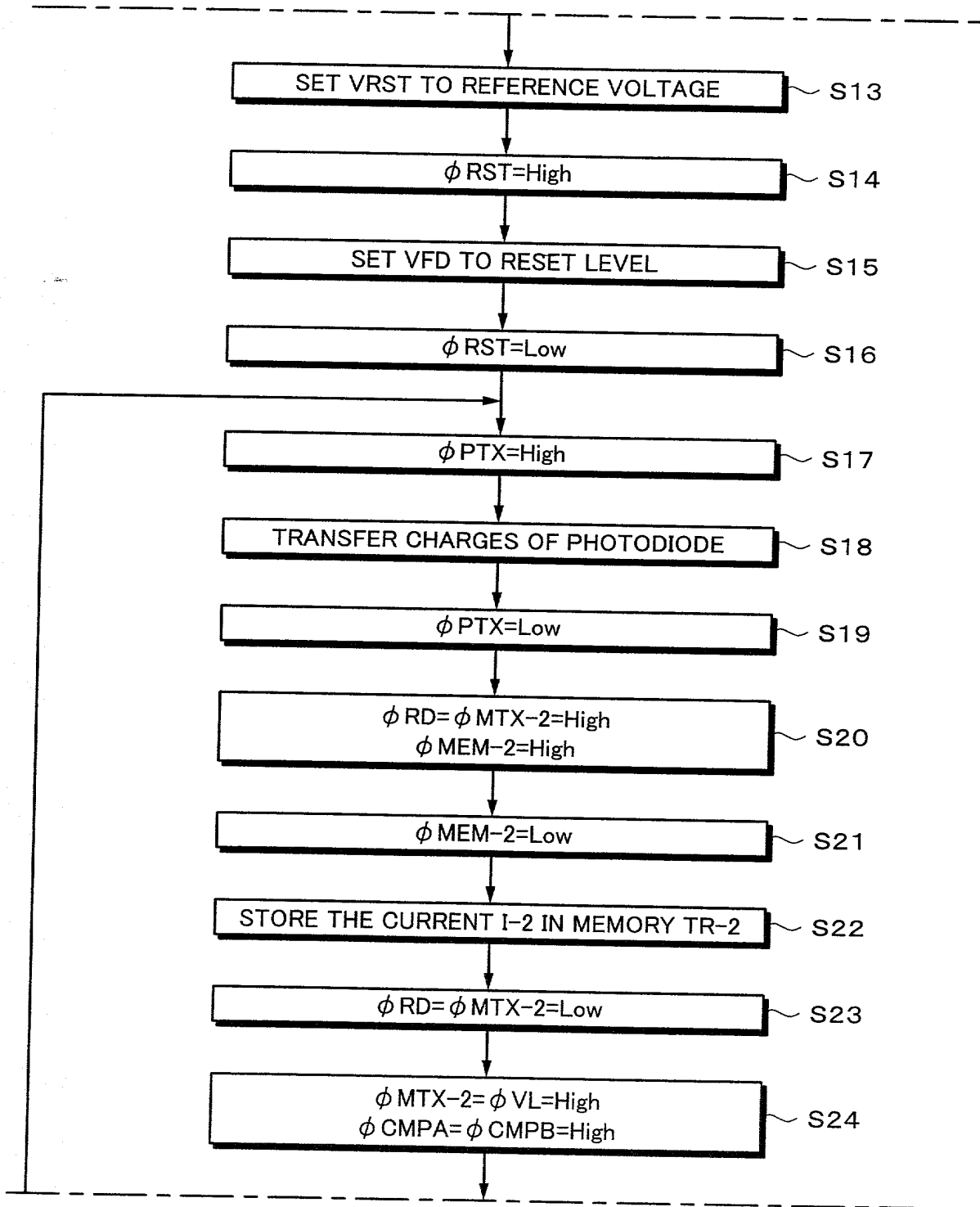
Fig. 9B

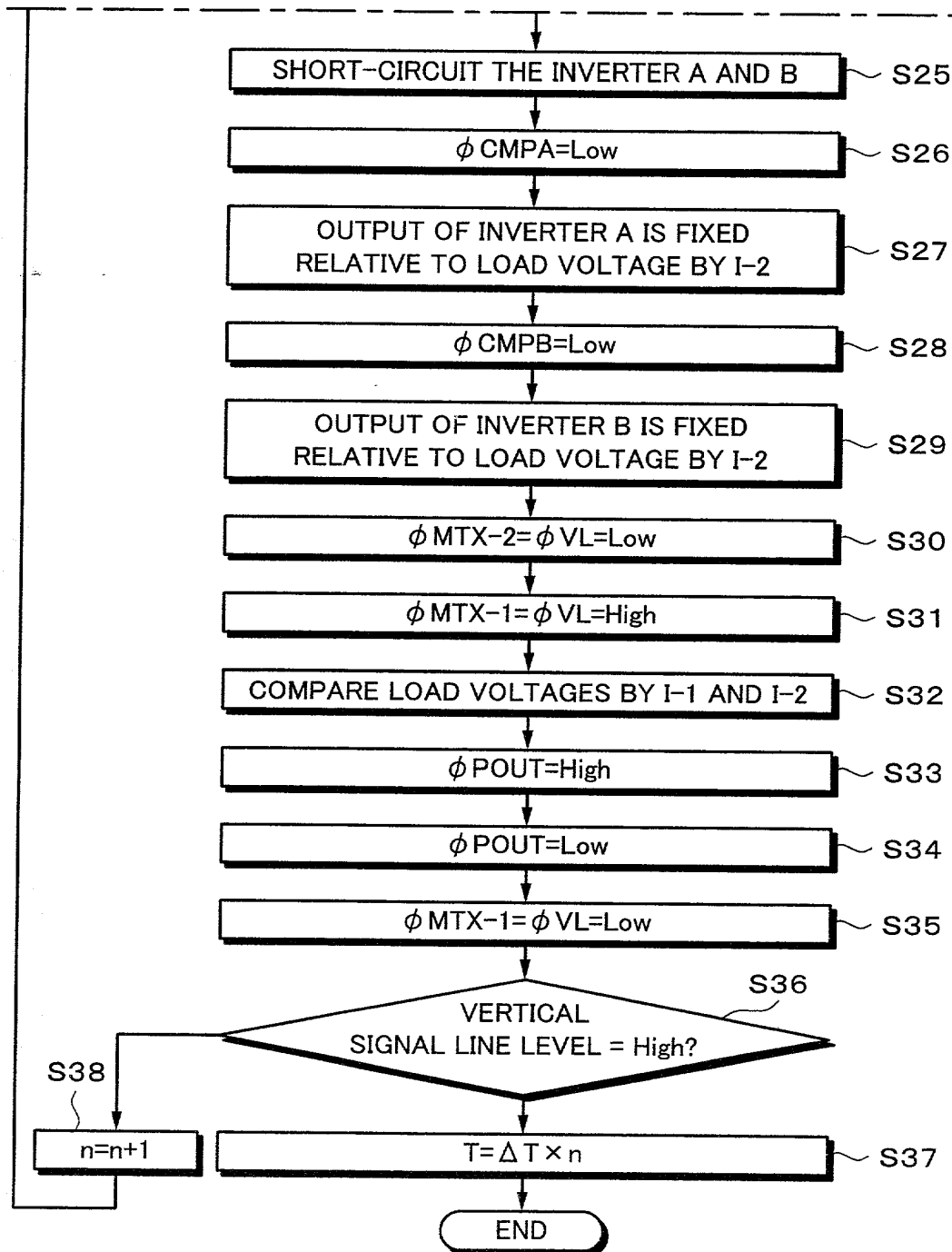
Fig. 9C

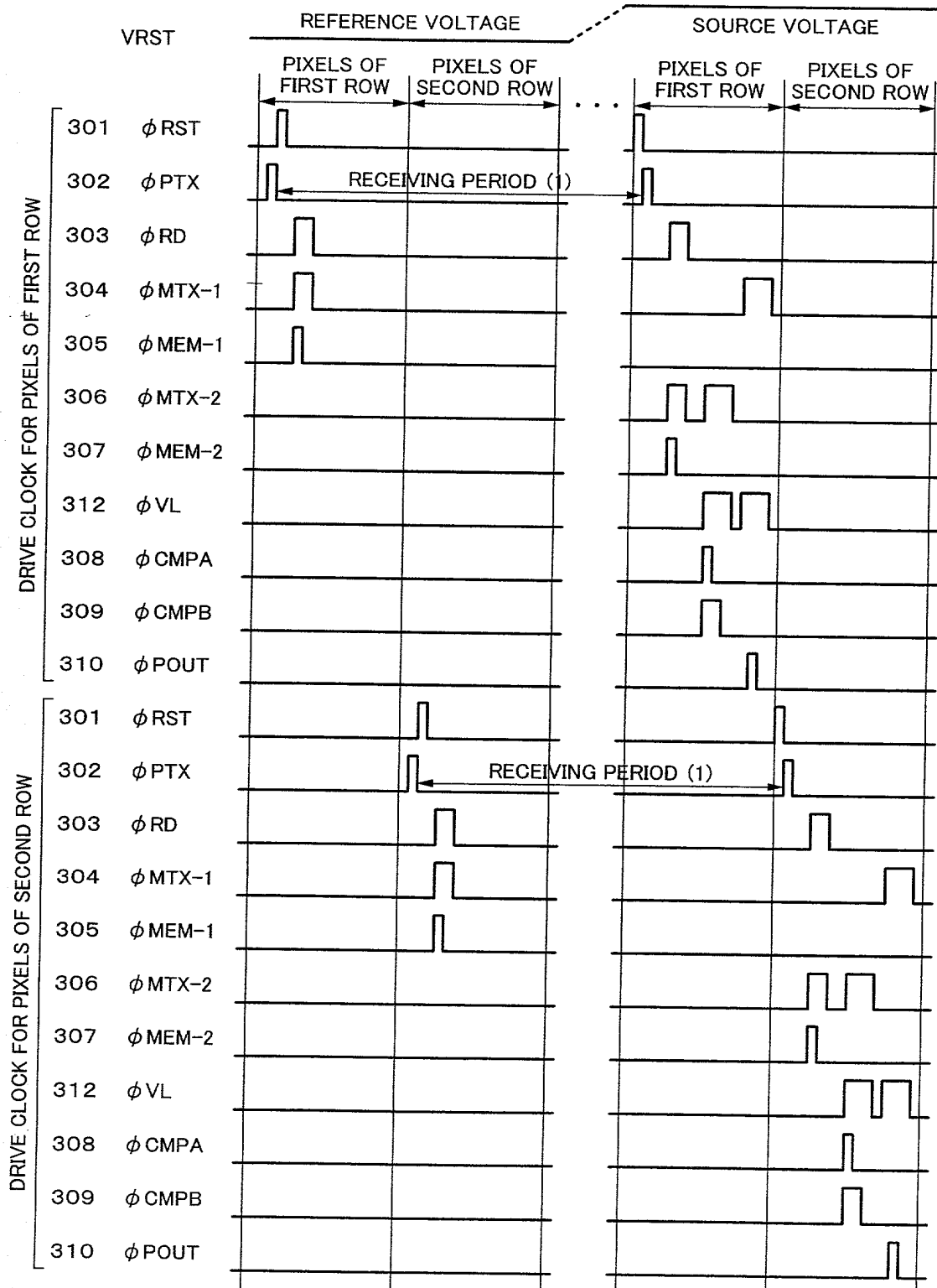
Fig. 10

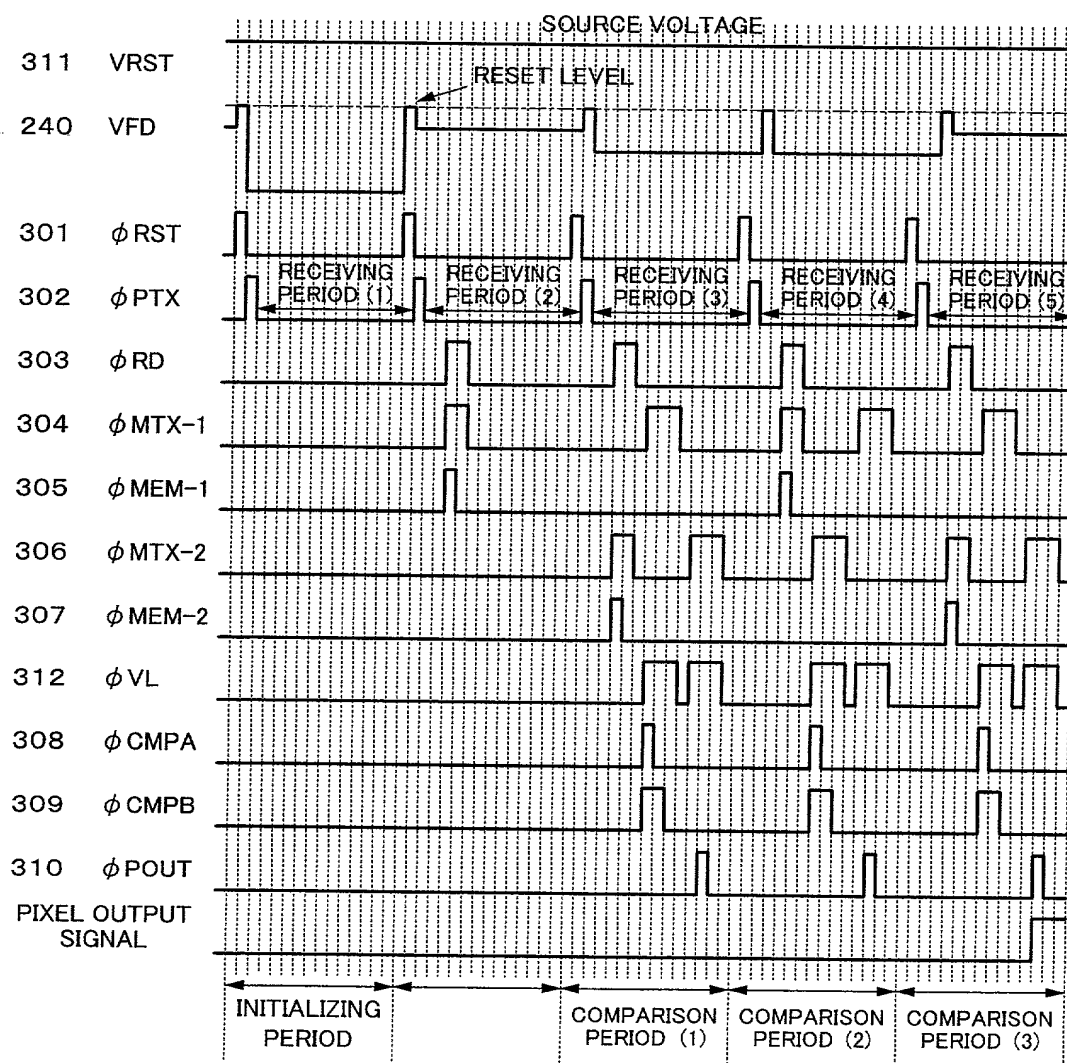
Fig. 11

Fig. 12A

Fig. 12

Fig. 12A

Fig. 12B

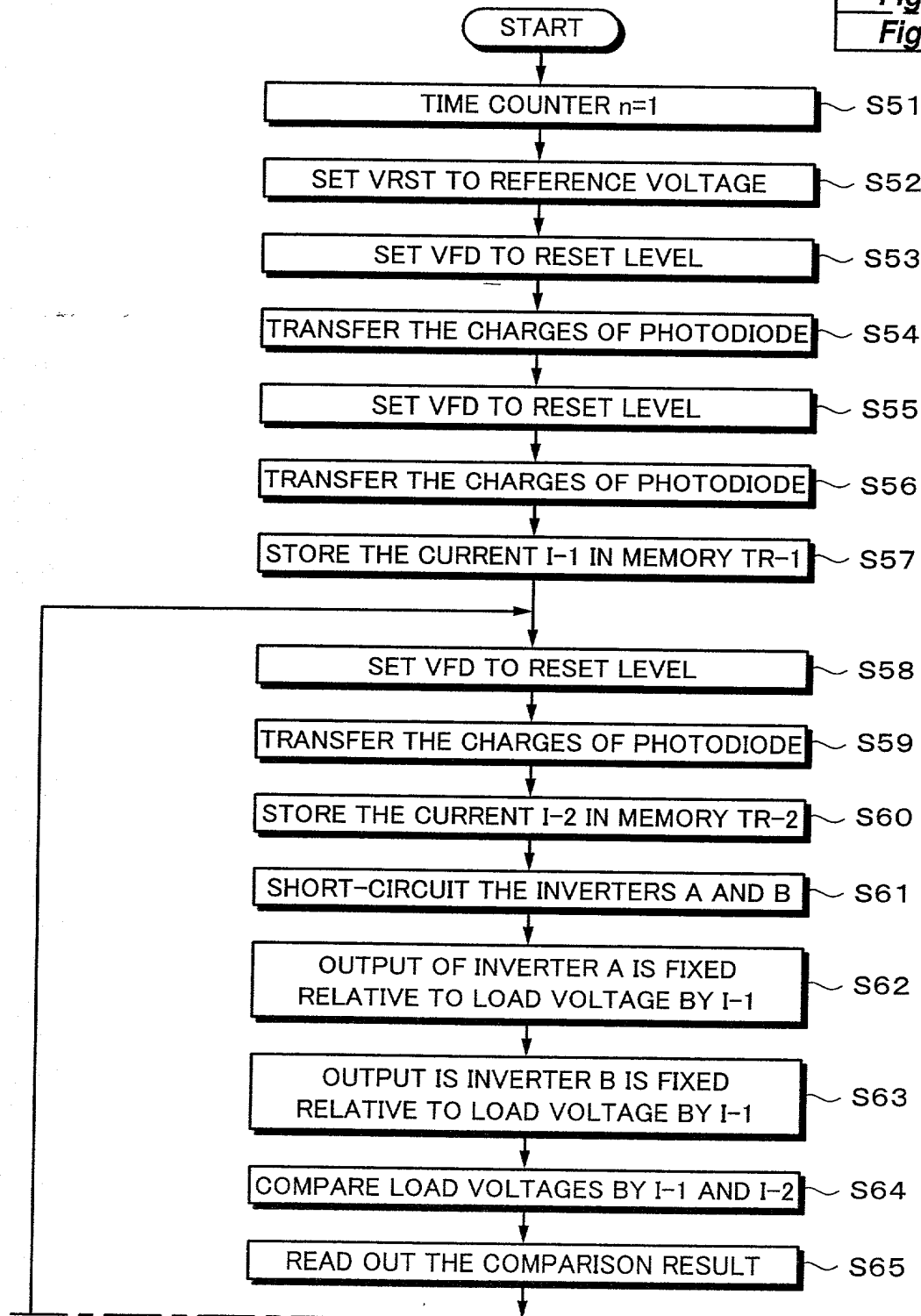


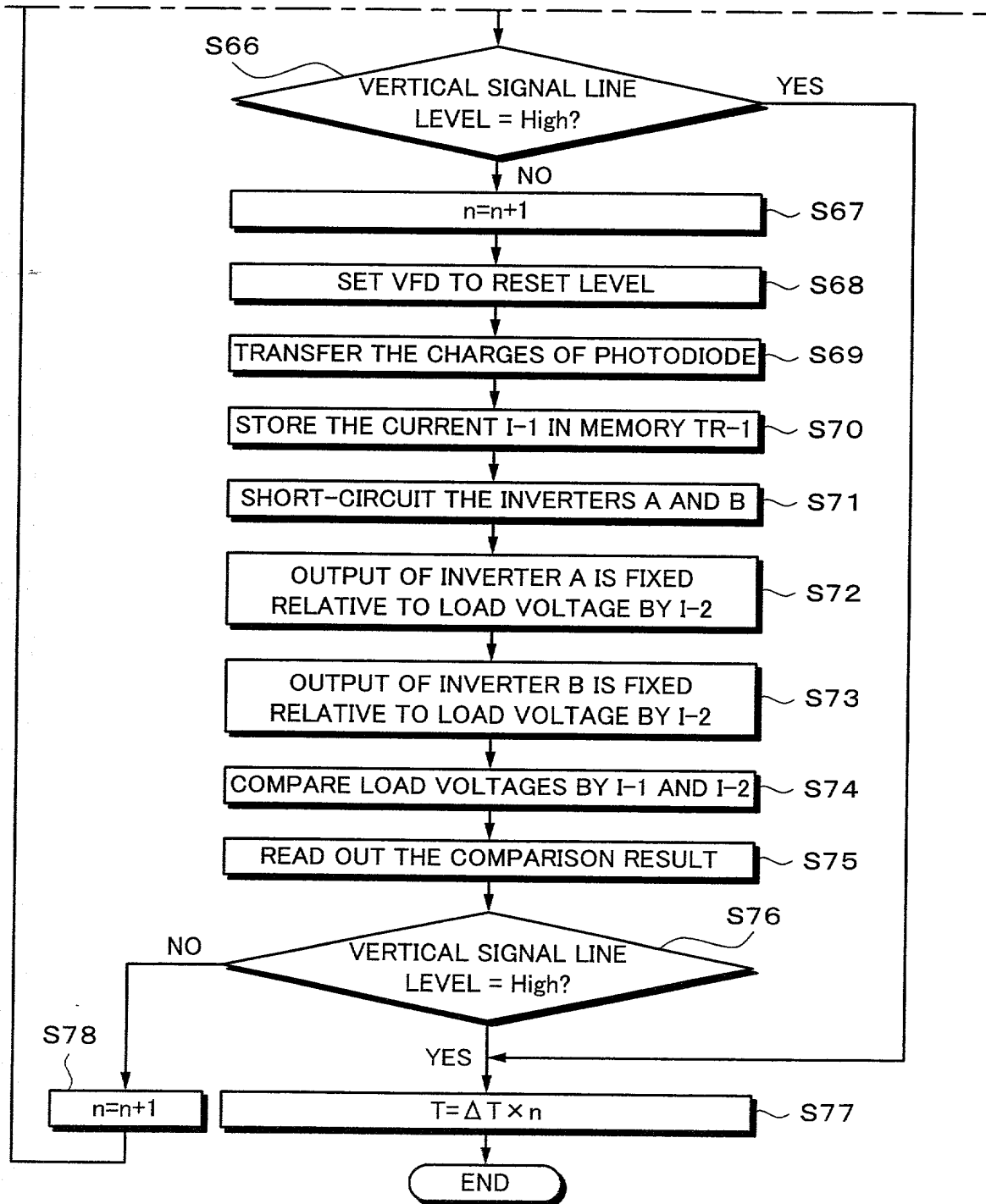
Fig. 12B

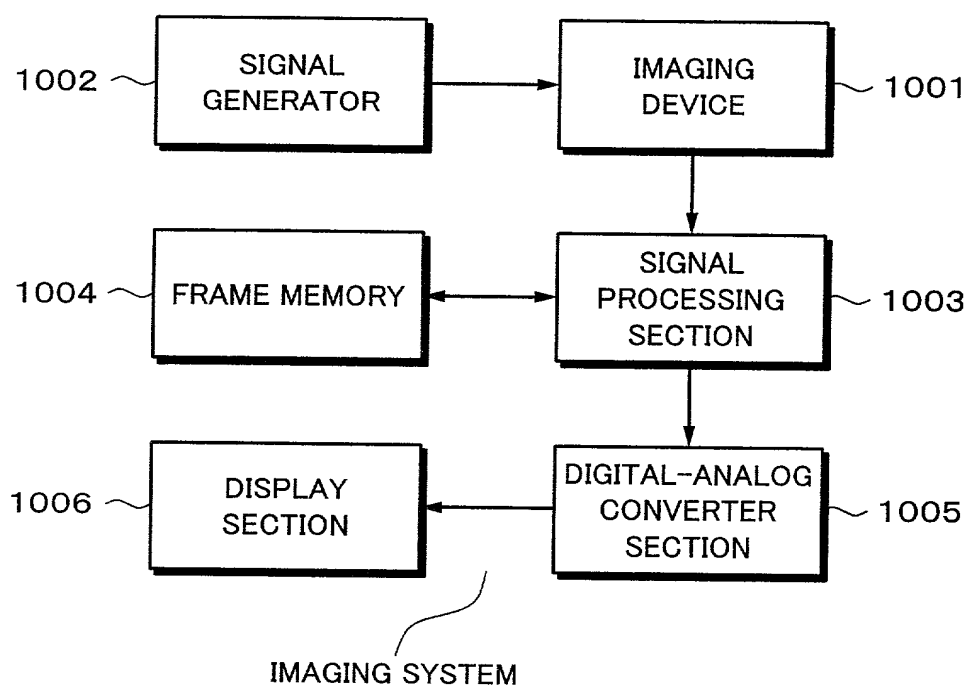
Fig. 13

Fig. 14

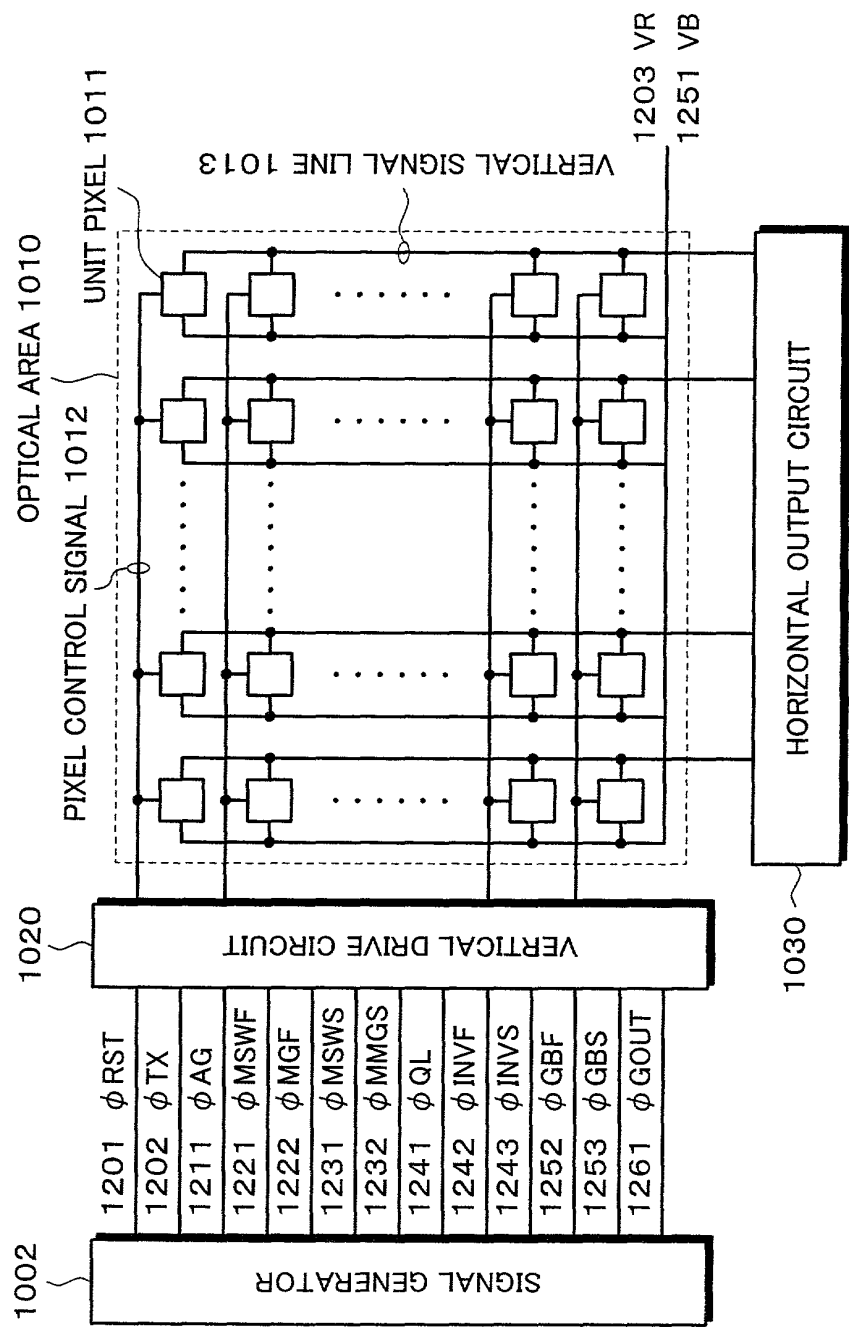


Fig. 15

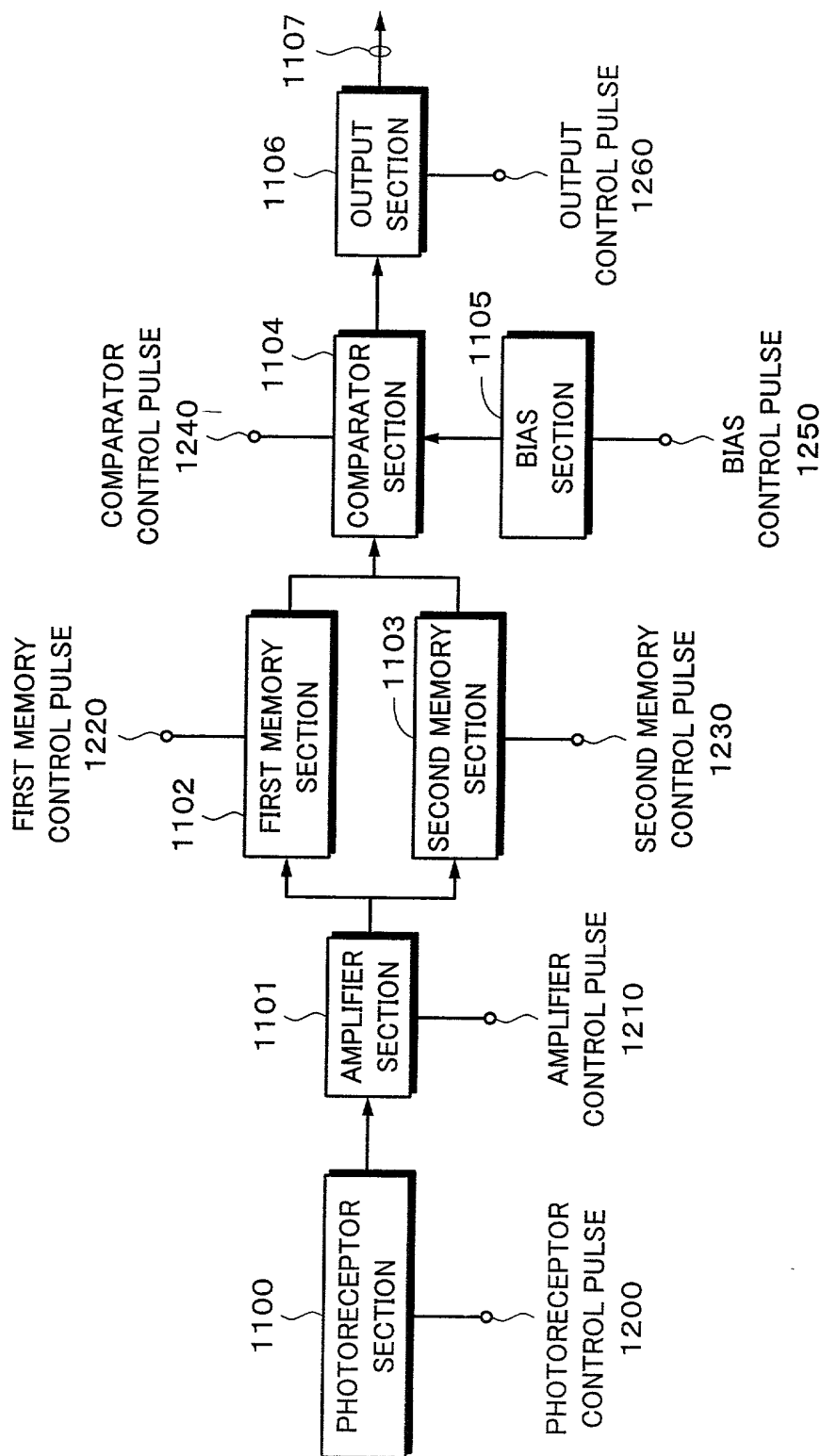


Fig. 17

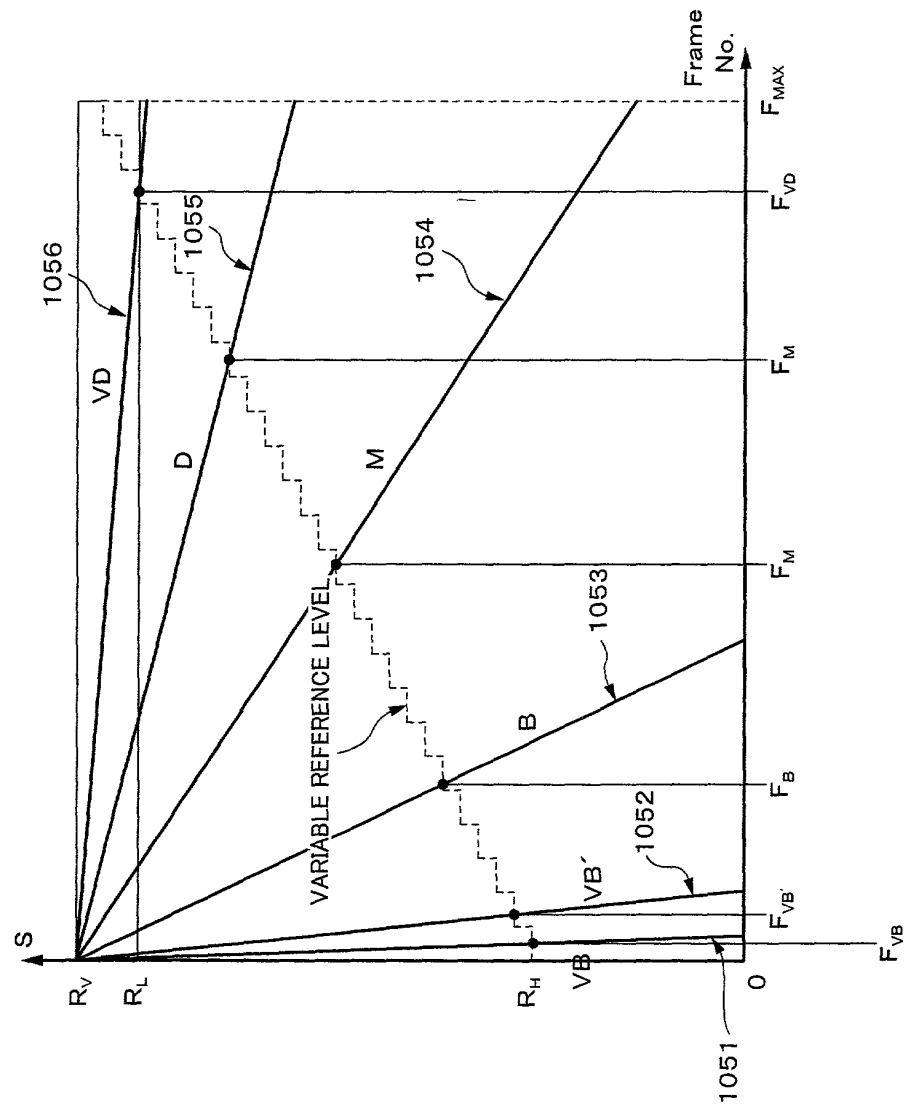


Fig. 18

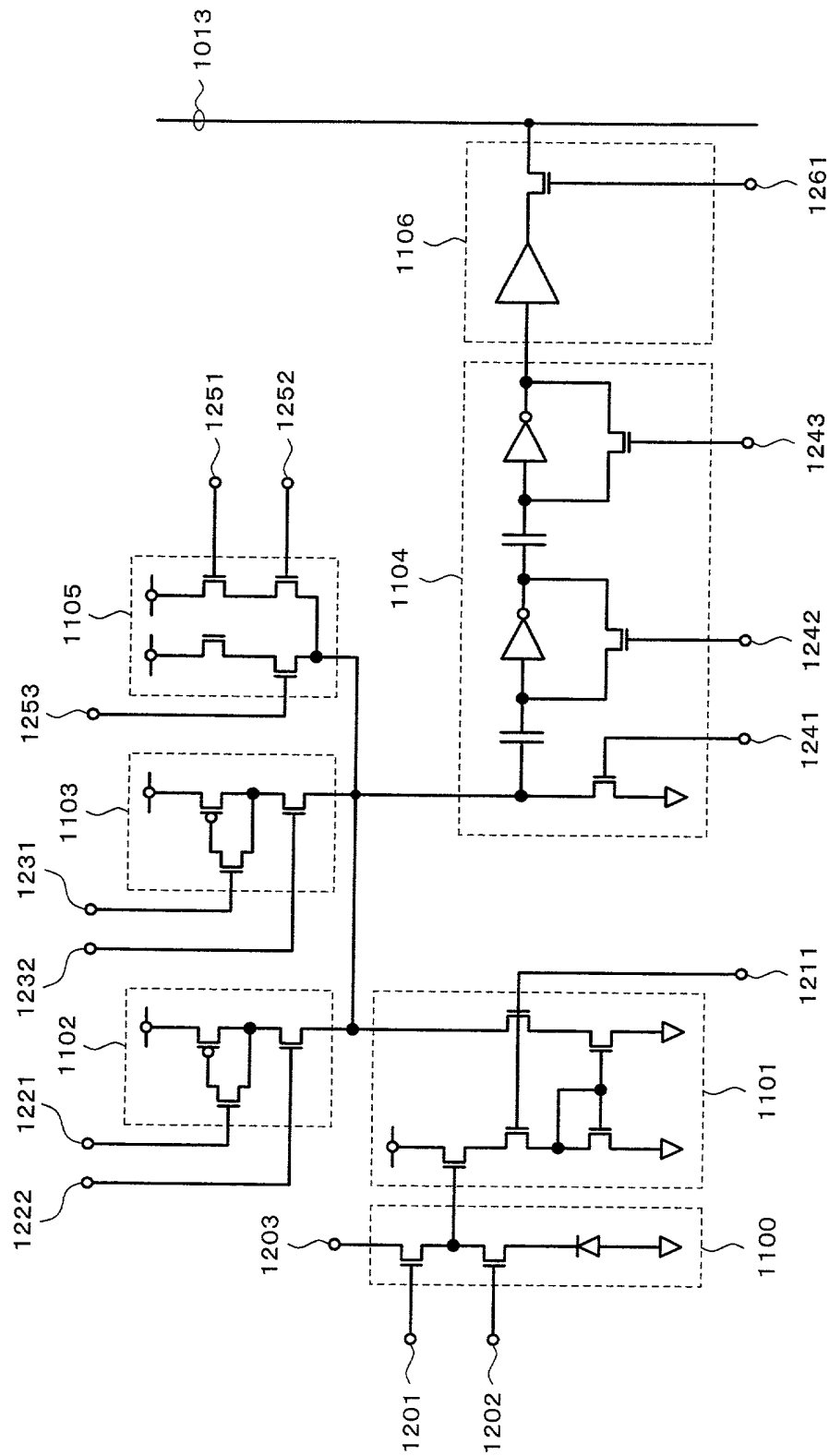


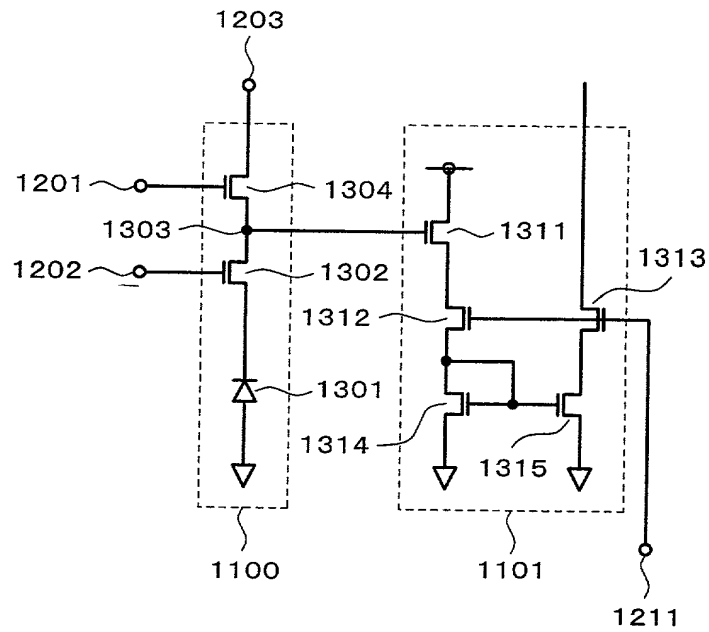
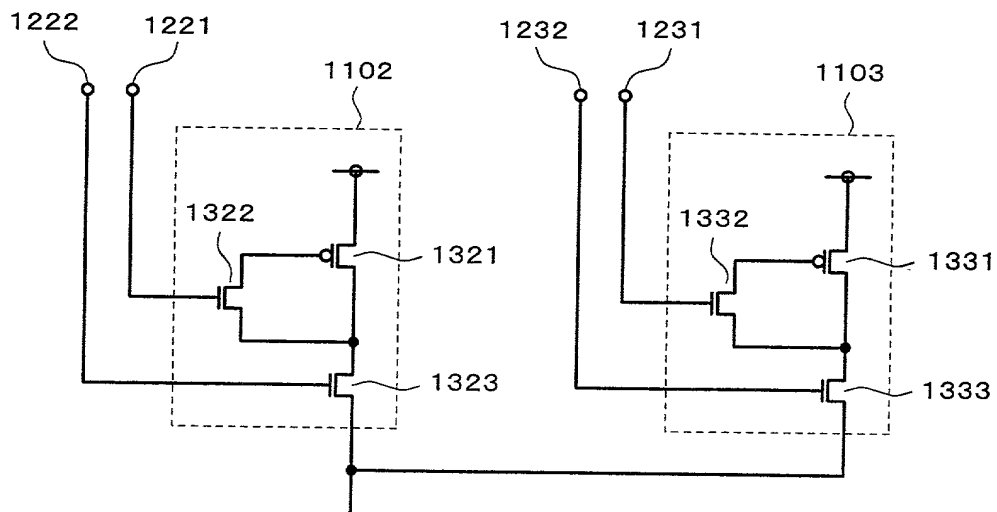
Fig. 19**Fig. 20**

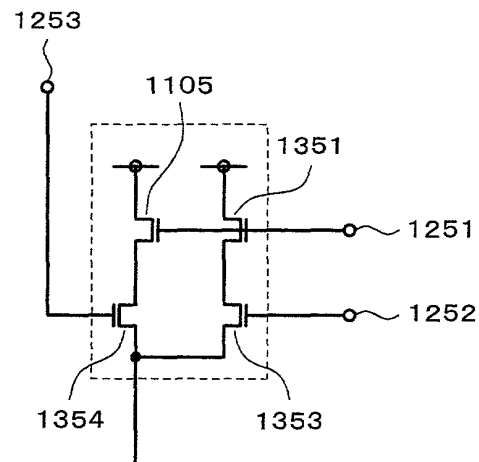
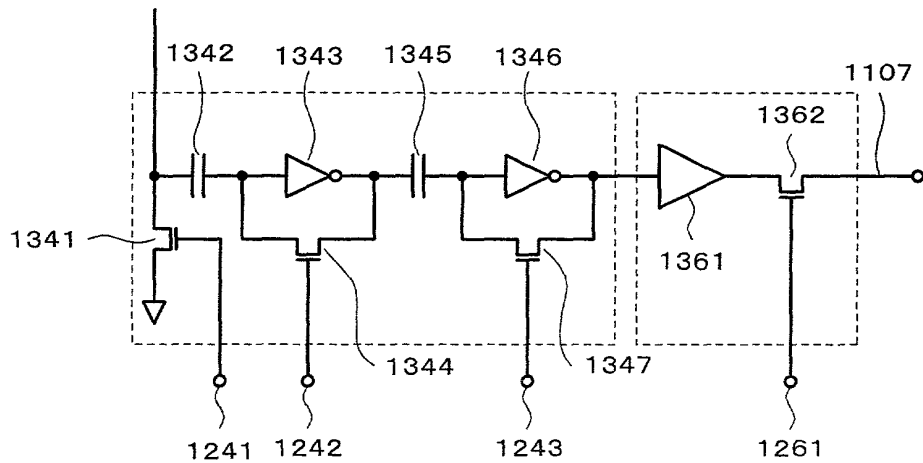
Fig. 21**Fig. 22**

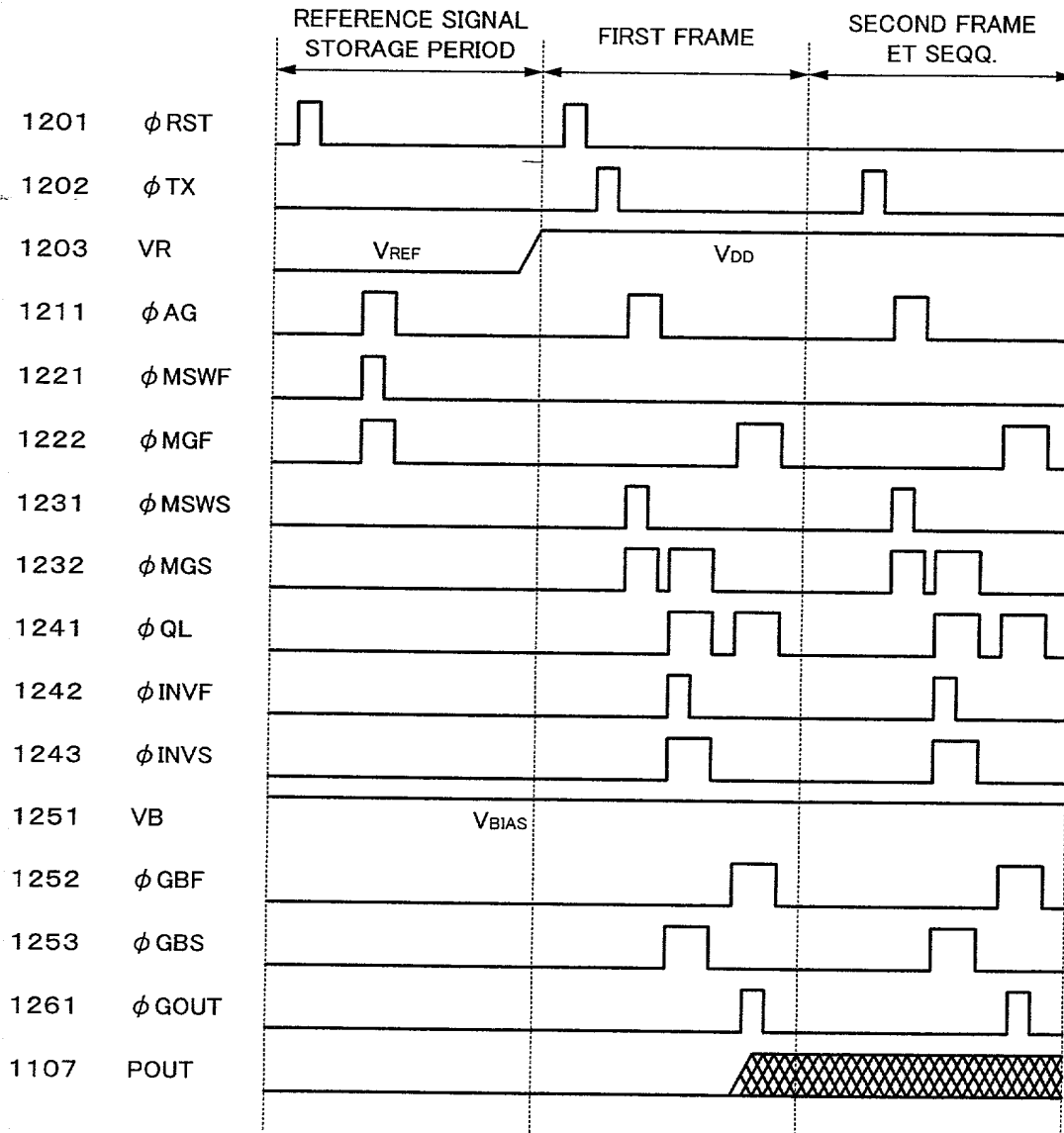
Fig. 23

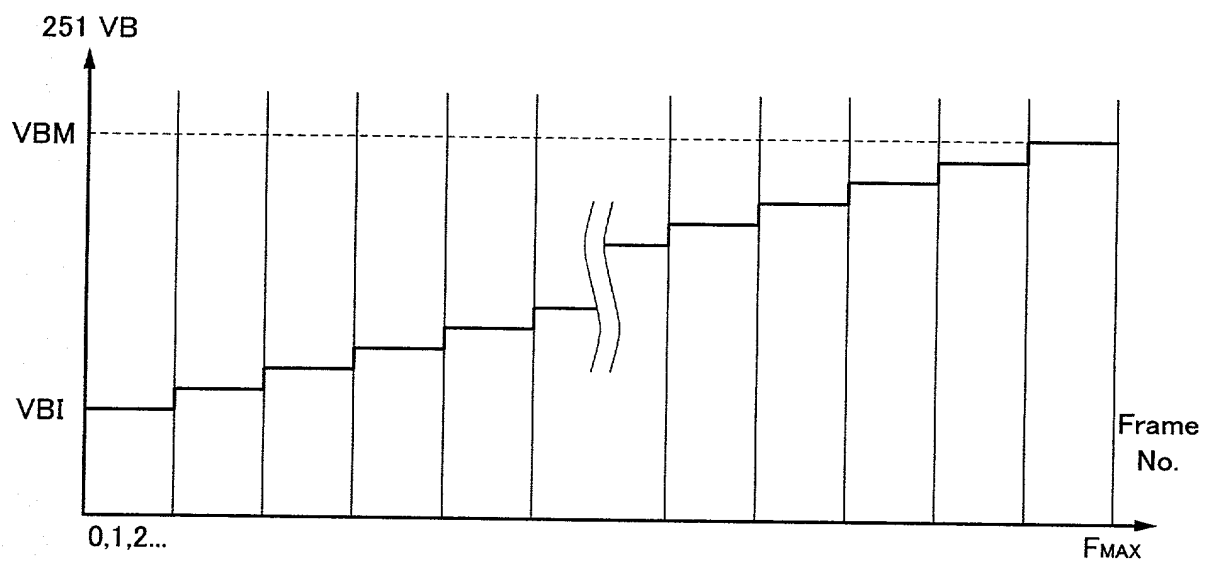
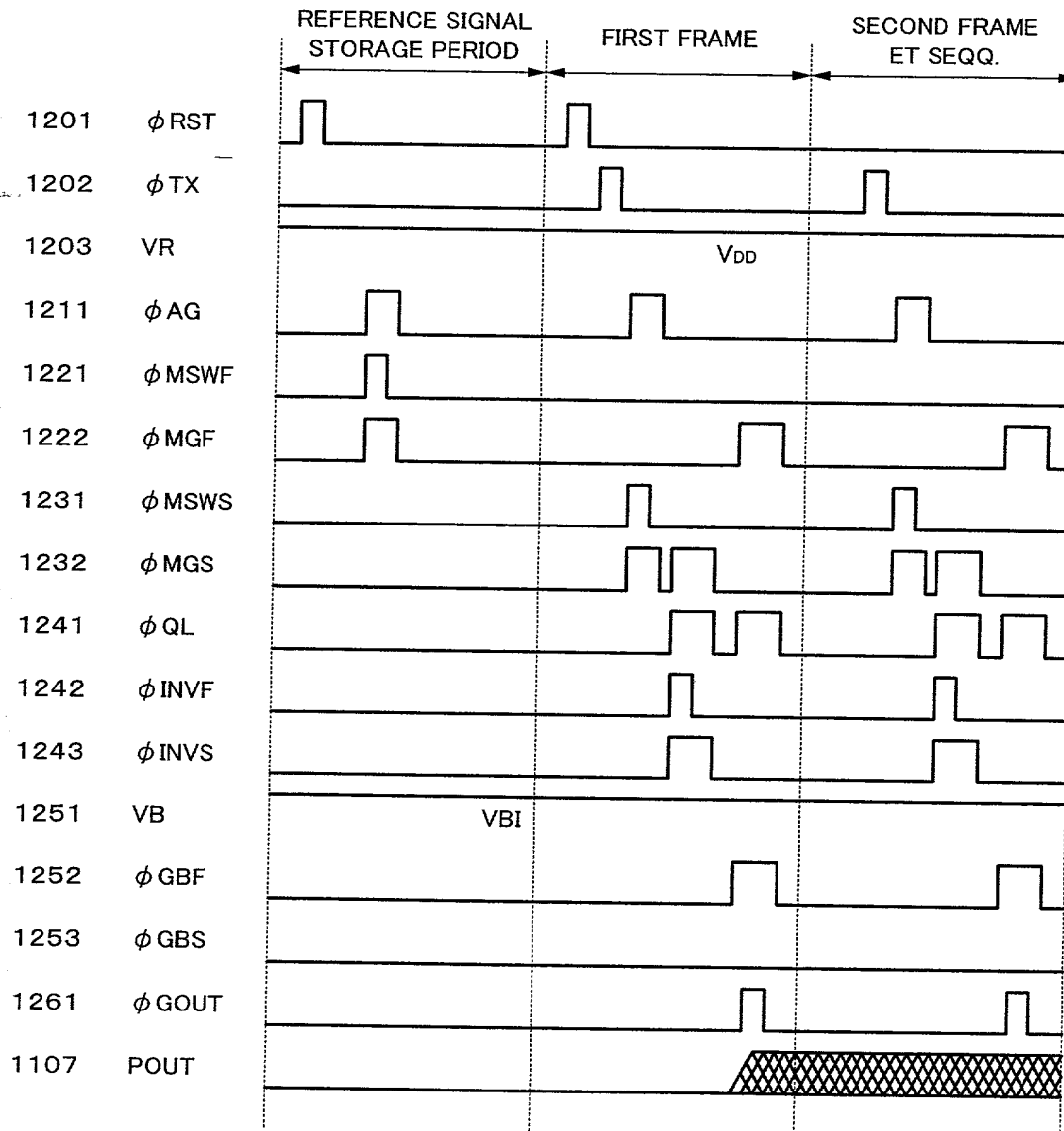
Fig. 24

Fig. 25

DESCRIPTION OF REFERENCE NUMERALS

1	PIXEL
2	DRIVE CLOCK GENERATOR
3	VERTICAL DRIVE CIRCUIT
10	PHOTORECEPTOR SECTION
20	FIRST AMPLIFIER SECTION
30	SECOND AMPLIFIER SECTION
40	MEMORY SECTION
50	LOAD AND ARITHMETIC SECTION
60	OUTPUT SECTION
1001	IMAGING DEVICE
1002	SIGNAL GENERATOR
1003	SIGNAL PROCESSING SECTION
1004	FRAME MEMORY
1005	DIGITAL-ANALOG CONVERTER SECTION
1006	DISPLAY SECTION
1011	UNIT PIXEL
1012	PIXEL CONTROL SIGNAL
1013	VERTICAL SIGNAL LINE